



PTO/SB/08B (10-01)

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Substitute for form 1449B/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 1

Complete if Known

Application Number	09/749,284
Filing Date	December 26, 2000
First Named Inventor	Gene R. Anderson
Group Art Unit	2874
Examiner Name	Unknown
Attorney Docket Number	1613370-0005

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher city and/or country where published	T ²
GRW		BUCKMAN et al., Parallel Optical Interconnects, Proc. Hot Interconnects VI, Stanford, CA, USA, Aug. 13-15, 1998, pp. 137-143	
GRW		ROSENBERG et al., The PONI-1 Parallel-Optical Link, 49th Electronic Components & Technology Conference, San Diego, CA, June 1-4, 1999, pp. 763-769	
GRW		GIBONEY et al., The PONI Optoelectronic Platform, IEEE Lasers and Electro-Optics Society 1999 Annual Meeting, San Francisco, CA, November 8-9, 1999, pp. 25-26	
GRW		BUCKMAN et al., Parallel Optical Interconnects, Conference on Lasers and Electro-Optics, San Francisco, CA, May 11, 2000, pp. 1-3	
			TC 2800 MAIL ROOM
			JUL 18 2002
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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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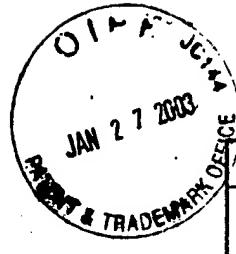
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PTO/SB/08A (10-96)

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Substitute for form 1449A/PTO			Complete If Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT			Application Number	09/749,284
			Filing Date	December 26, 2000
			First Named Inventor	Gene R. Anderson
			Group Art Unit	2877
(use as many sheets as necessary)			Examiner Name	Valentin, Juan D.
Sheet	1	of	2	Attorney Docket Number
			1613370-0005	

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
AH		US - 4,145,110	03-20-1979	Szentesi	
		US - 4,329,190	05-11-1982	Berg et al.	
		US - 4,479,698	10-30-1984	Landis et al.	
		US - 4,709,979	12-01-1987	Spodati et al.	
		US - 4,729,623	03-08-1988	Mery	
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		US - 5,630,988	05-20-1997	Stolp	
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		US - 5,963,696	10-05-1999	Yoshida et al.	
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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ³ Number ⁴			
AH		GB 1 304 428 A	01-24-1973	General Elec.	
		JP 56 046573	04-17-1981	Hitachi Ltd.	
		JP 56 079468	06-30-1981	Matsushita	
		JP 60 156023	08-16-1985	Tateishi Denki	
		JP 01 073304	03-17-1989	Furukawa Elec.	
		DE 40 02 370	08-01-1991	Standard Elek.	
		JP 05 102529	04-23-1993	NEC Corp.	
		EP 0 589 622	03-30-1994	American Tele.	
		JP 08 082724	03-26-1996	Fujitsu	
		JP 09 043456	02-14-1997	NEC Corp.	
		JP 09 061683	03-07-1997	Fuji Elec.	
		JP 09 197197	07-31-1997	Furukawa Elec.	
		JP 10 186162	07-14-1998	Furukawa Elec.	
		WO 98 31080	07-16-1998	W.L. Gore et al.	
		JP 10 239569	09-11-1998	Asahi Glass Co.	
		JP 10 197751	10-31-1998	Hitachi Cable	
		WO 99 39230	08-05-1999	Bueschelberger	
		EP 1 028 340	08-16-2000	Nippon Elect.	

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>ADH</i>		NAGAHORI, et al., 1-Gbyte/sec Array Transmitter and Receiver Modules for Low-Cost Optical Fiber Interconnection, 996 Electronic Components & Technology Conference, 46 th Proceedings, pp. 255-258	
		NIBURG et al., A Complete Sub-System of Parallel Optical Interconnects for Telecom Applications, 1996 Electronic Components & Technology Conference, 46 th Proceedings, pp. 259-263	
		de PESTEL et al., Parallel Optical Interconnections for Future Broad Band Systems, Based on the "Fiber in Board Technology", 1996 Electronic Components & Technology Conference, 46 th Proceedings, pp. 264-266	
		WONG et al., OptoElectronic Technology Consortium (OETC) Parallel Optical Data Link: Components, System Applications and Simulation Tools, 1996 Electronic Components & Technology Conference, 46 th Proceedings, pp. 269-278	
		LEBBY et al., Characteristics of VCSEL Arrays for Parallel Optical Interconnects, 1996 Electronic Components & Technology Conference, 46 th Proceedings, pp. 279-291	
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		LIU et al., Polymer Optical Interconnect Technology (POINT) – Optoelectronic Packaging and Interconnect for Board and Backplane Applications, 1996 Electronic Components & Technology Conference, 46 th Proceedings, pp. 308-315	
		SWIRHUN et al., The P-VixeLink™ Multichannel Optical Interconnect, 1996 Electronic Components & Technology Conference, 46 th Proceedings, pp. 316-320	
<i>ADH</i>		CARSON et al., Low-Power Modular Parallel Photonic Data Links, 1996 Electronic Components & Technology Conference, 46 th Proceedings, pp. 321-326	

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4 March 03

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